

Upgrade of the Fast Beam Conditions Monitor BCM1F

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Introduction

The BCM1F detector is one of the subsystems of the CMS Beam Conditions and Radiation Monitoring (BRM) system. Designed to monitor the flux of particles with a time resolution of a nanosecond, BCM1F is able to fast diagnose adverse beam conditions, such as beam losses, that could be harmful for the detector components. It monitors luminosity as well as vacuum and beam-background conditions at real time in the region close to the CMS pixel detector.



Data acquisition system in 2012

In each module, a sensor is connected to a radiation hard front-end ASIC. The signals are pre-amplified, shaped, converted into analog optical signals and transmitted to a CMS service room where they are converted back to electrical signals, then discriminated, digitised and processed. Information on hit rates, coincidences, signal shape and time over an orbit are provided by a system made up of scalers, a look-up table (LUT), a flash analogue-to-digital converter (ADC) board and a time-to-digital converter (TDC) board. The BCM1F chain is illustrated in Fig.2.

Fig.2: Schematic layout of the BCM1F electronics architecture as in 2012.

Beam-gas Background

BPTX signals are used to produce gates, with time windows, to measure: the rate of

collision products for luminosity measurement; the non-colliding bunches beam-halo rate for vacuum conditions; and rates halfway between two collisions to estimate the albedo contributions.

BCM1F performance with data



Fig.1: Schematic showing the location of BCM1F. The pictures show the sensors and front-end before and after installation. BCM1F uses sCVD diamond sensors that fulfil the requirements of size, of nanosecond time resolution for fast response; and of radiation hardness. Eight $5 \times 5 \times 0.5$ mm³ diamond sensors are mounted ~5 cm from the

sensors are mounted ~5 cm from the beam line on two planes located on both sides and ~1.83 m away from the CMS interaction point (Fig.1), which is an optimal location for separation of incoming and outgoing particles. BCM1F was installed in 2008 and in operation since then until 2013.

Beam background measurements

BCM1F delivers to the control rooms two background numbers to ensure safe CMS & LHC operation: the total flux of particles in the inner region of the pixel detector and the charged grand diamond the pixel detector and the pixel detector and the pixel detector and the pixel detector and the pixel diamond the pixel detector and the pixel diamond the pixel detector and the pixel diamond th



runs using the Van der Meer (VdM) scan technique to ^{ig}perform an absolute calibration of the fuminosity. A measurement of the mean overlap and the effective size of the xcomponent of the beam is shown in Fig.3. Fig.4 shows the measurement of the instantaneous luminosity with BCM1F during an LHC fill after calibration and corrections for inefficiencies. For comparison the HF luminosity is also shown.

BCM1F Upgrade Motivation and Plans

After the LS1 the LHC will operate at higher luminosity, of at least 10^{34} cm⁻²s⁻¹, and at higher energy, 6.5 TeV per beam. Moreover, the time between two bunch crossings will decreas system has limited performance under these conditions. A new system has limited performance under these conditions. A new system has limited performance under these conditions. A new system has limited performance under these conditions. A new system has limited performance under these conditions. A new system has limited performance under these conditions. A new system has limited performance under these conditions. A new system has limited performance under these conditions. A new system has limited performance under development and to be operational in $\begin{bmatrix} 36 & -36 &$



New front-end ASIC

A new fast and radiation-hard front-end ASIC has been developed by Dominik

multiple-hit detection is under development and to be operational in The upgraded system envisage new front-end and back-end with ch sources of inefficiencies and give more precise information on condi example of inefficiency is shown in Fig.5. The efficiency losses obse

identified as due to the saturation of the preamplifier leading to long The gain in signal has shown to be dependent on the temperature o and temperature monitoring of the lasers. A new carriage with such such





Fig.5: BCM1F LumiOR rates as a function of the LHC bunch number.

BCM1I



1350 1400 1450 1500

1550 1600

Fig.6: Overshoot pulse due to the saturation of the frontend.



New sensors

The number of diamond sensors will increase from 8 to 24, giving a higher degree of redundancy. Each diamond will be split in two pads to suppress the multiple hit rate at high pile-up.



New carriage

A new mechanical structure using carbon fibre is being developed to support the diamonds, ASIC and services. The laser drivers will be moved to a lowerradiation part of the carriage to avoid radiation damage. Four integratedC-shape PCB are planned, each with:

New back-end electronics

1300

The expected very high rates after the LS1 and the requirement of count rate measurement with minimal deadtime and pile-up losses need a dedicated backend equipment. The system should identify hits and their arrival time with respect to the LHC orbit trigger. An automatic calibration system is envisaged. Other main features are described below.

Multiple Gate and Delay (MGD)

MGD units will be used for counting rates of collisions, beam-gas and albedo events in gated time windows. The albedo will become more important and should be done online. The logics for beam-gas should be revised as no non-colliding bunches is foreseen.

Histogram Unit (RHU)

A real time histogramming unit (RHU) board is under development and will allow the measurement of bunch-by-bunch luminosity online and possibly for postmortem analysis. The first units produced were tested and validated in 2012 with LHC p-Pb collisions (Fig.8). Further developments on hardware and firmware are ongoing to be fully integrated in the CMS Lumi DAQ system.

FPGA-based back-end

The use of modern digitisers with built-in FPGA capabilities is under study as a more suitable solution for the expected running conditions during the LHC Run II. Peak finders and deconvolution (Fig.9) algorithms programmed in the FPGA could give, without deadtime, better distinguishability between signals with small separations, very good time resolution and reduced data size.



Fig.9: Non-FPGA example: (Left) New front-end simulated signals from the ADC searched using Root

- 6 BCM1F diamonds (each split in two channels)
- 2 BCM1L diamonds
- Temperature monitoring
- Integrated cooling for the lasers
- Integrated transport to linear laser drivers and analog opto-hybrids.



Fig.8: BCM1F raw hits distribution as a function of the arrival time with RHU.

peak finder algorithm. (Right) The same signals after Root deconvolution and peak finder algorithms.

Simulations

In order to shape the architecture of the back-end electronics as well as understand better the sources of systematics, BCM1F is being integrated in the CMS geometry to use simulated minimum bias events with pile-up for studies of rates, efficiencies, linearity etc.



Fig.10: A simulated 20 GeV proton hits one of the BCM1F sensors

Outlook

The BCM1F detector has proven to be a robust and reliable beam-background and online luminosity monitor with underlying characteristics to cope with significant increases of the luminosity. However an upgrade of the system is mandatory not only to cope with the more severe conditions in which the LHC machine will operate after LS1 but also to attenuate sources of inefficiencies that would compromise the accuracy of the measurements. The BRM group is performing developments and studies in various fronts in the upgrade of BCM1F to ensure that the detector will continue to provide accurate, reliable and valuable information to the CMS experiment and the LHC machine.